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About

Dr. Haris Ataullah is working as Lab Engineer in the College of Electrical & Mechanical Engineering. Dr. Haris Ataullah has published 3 research articles & conference papers having a citation count of 32, carried out 0 projects and filed 0 intellectual property.

Qualifications

MS in Control system NUST, Islamabad , Pakistan	2019 - 2022
BSc in Power N.W.F.P University of Engineering & Technology Peshawar , Pakistan	2015 - 2019

Experience

Lab Engineer College of Electrical & Mechanical Engineering	2025 - 2025
Lab Engineer College of Electrical & Mechanical Engineering	2023- Present

Research Articles

High gain coupled inductor SEPIC based boost inverter using extended SPWM <i>Haris Ataullah Taosif Iqbal Ihsan Ullah Khalil Taimoor Hassan Enas Ali Saad A. Mohamed Abdelwahab</i> <i>Energy Reports</i> , Volume 10, Pages 4013-4024 Impact Factor: 5.2 Quartile: 2 Citations: 3 DOI: https://doi.org/10.1016/j.egy.2023.10.057	2023
Analysis of the Dual Active Bridge-Based DC-DC Converter Topologies, High-Frequency Transformer, and Control Techniques <i>Haris Ataullah Taosif Iqbal Ihsan Ullah Khalil Usman Ali Vojtech Blazek Lukas Prokop Nasim Ullah</i> <i>Energies</i> , Volume 15(23), Article Number 8944 Impact Factor: 3.252 Quartile: 3 Citations: 24 DOI: 10.3390/en15238944	2022
Analysis and Verification of Leakage Inductance Calculation in DAB Converters Based on High-Frequency Toroidal Transformers under Different Design Scenarios <i>Haris Ataullah Taosif Iqbal Ihsan Ullah Khalil Al-Sharef Mohammad Nasim Ullah Mohamed Emad Farrag</i> <i>Energies</i> , Volume 15(17), Article Number 6176 Impact Factor: 3.252 Quartile: 2 Citations: 5 DOI: https://doi.org/10.3390/en15176176	2022