

# Dur-E-Shahwar Kundi

Assistant Professor  
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## About

Dr. Dur-E-Shahwar Kundi is working as Assistant Professor in the Pakistan Navy Engineering College. Dr. Dur-E-Shahwar Kundi has a PhD in Information Security. Dr. Dur-E-Shahwar Kundi has published 7 research articles & conference papers having a citation count of 78, carried out 0 projects and filed 0 intellectual property.

## Qualifications

<b>PhD in Information Security</b> NUST, Islamabad , Pakistan	2010 - 2016
<b>MS in Communication</b> NUST, Islamabad , Pakistan	2007 - 2009
<b>BE in Power</b> NUST, Islamabad , Pakistan	2003 - 2006

## Experience

<b>Assistant Professor</b> Pakistan Navy Engineering College	2021- Present
<b>Assistant Professor</b> Pakistan Navy Engineering College	2019 - 2019
<b>Assistant Professor</b> Pakistan Navy Engineering College	2012 - 2019
<b>Lecturer</b> Pakistan Navy Engineering College	2010 - 2012
<b>Lab Supervisor</b> Pakistan Navy Engineering College	2007 - 2010
<b>Asisstant Professor</b> NUST , PNEC	2012 - 2018
<b>Lecturer</b> NUST , PNEC	2010 - 2012
<b>Lab Engineer</b> NUST , PNEC	2007 - 2010

## Awards

<b>President Gold Medal</b> For being best in academics M.S.	2010
<b>President Gold Medal</b> For being best in academics B.E.	2007

- APAS: Application-Specific Accelerators for RLWE-Based Homomorphic Linear Transformations** 2021  
*Dur-e-Shahwar Kundi Song Bian Kazuma Hirozawa Weiqiang Liu Takashi Sato*  
*IEEE Transactions on Information Forensics and Security*, Volume 16, Pages 4663-4678  
**Impact Factor:** 7.178 | **Quartile:** 1 | **Citations:** 12  
**DOI:** 10.1109/TIFS.2021.3114032
- A Pareto-Optimal Multi-filter Architecture on FPGA for Image Processing Applications** 2019  
*Majida Kazmi Arshad Aziz Dur-E-Shahwar Kundi*  
*Circuits Systems and Signal Processing*, Volume 38, Issue 10, Pages 4762-4786  
**Impact Factor:** 1.681 | **Quartile:** 3 | **Citations:** 2  
**DOI:** 10.1007/s00034-019-01083-4
- Multi-Resolution Transforms Based Hybrid Feature Extraction Technique for Differentiating Glioma Grades.** 2018  
*Razia Zia Pervez Akhtar Arshad Aziz Dur-E-Shahwar Kundi*  
*International Journal of Wavelets, Multiresolution and Information Processing*, Volume 16, No. 06, Article Number 1850048  
**Impact Factor:** 0.523 | **Quartile:** 4  
**DOI:** 10.1142/S0219691318500480
- A low-power SHA-3 designs using embedded digital signal processing slice on FPGA** 2016  
*Dur-E-Shahwar Kundi Arshad Aziz*  
*Computers & Electrical Engineering*, Volume 55, Pages 138-152  
**Impact Factor:** 1.57 | **Quartile:** 3 | **Citations:** 20  
**DOI:** 10.1016/j.compeleceng.2016.04.004
- A high performance ST-Box based unified AES encryption/decryption architecture on FPGA** 2016  
*Dur-E-Shahwar Kundi Arshad Aziz Nassar Ikram*  
*Microprocessors and Microsystems*, Volume 41, Pages 37-46  
**Impact Factor:** 1.025 | **Quartile:** 3 | **Citations:** 24  
**DOI:** 10.1016/j.micpro.2015.11.015
- FPGA Based Compact and Efficient Full Image Buffering for Neighborhood Operations** 2015  
*Majida Kazmi Arshad Aziz Pervez Akhter Dur-E-Shahwar Kundi*  
*Advances in Electrical and Computer Engineering*, Volume 15, Issue 1, Pages 95-104  
**Impact Factor:** 0.459 | **Quartile:** 4 | **Citations:** 6  
**DOI:** 10.4316/AECE.2015.01014
- Resource efficient implementation of T-Boxes in AES on Virtex-5 FPGA** 2010  
*Dur-E-Shahwar Kundi Arshad Aziz Nassar Ikram*  
*Information Processing Letters*, Volume 110, Issue 10, Pages 373-377  
**Impact Factor:** 0.612 | **Quartile:** 4 | **Citations:** 14  
**DOI:** 10.1016/j.ipl.2010.03.004